

AMENDMENTS TO THE SPECIFICATION

Please replace the paragraph beginning on page 14, line 26 and ending on page 15, line 19, with the following amended paragraph:

Each of the charge amplifying ICs 34 has a number of charge amplifiers 34a and sample-and-hold (S/H) circuits 34b, each being connected to each of the elements 15a of the solid-state detector 10; and a multiplexer 34c for multiplexing the signal from each of the sample-and-hold circuits 34b. The electric current flowing out from the solid-state detector is converted to a voltage signal by each of the charge amplifiers 34a, which is then sampled at a given timing and held by each of the sample-and-hold circuits. Then, each of the voltage signals corresponding to each of the elements 15a held by each of the sample-and-hold circuits is sequentially outputted from the multiplexer 34c in the order in which corresponding elements 15a are arranged (which corresponds to a part of the main scanning). The signal sequentially outputted from the multiplexer 34c is inputted to the multiplexer 33, which sequentially outputs the inputted signals in the order in which the corresponding elements 15a are arranged to complete the main scanning. The signal sequentially outputted from the multiplexer 33 is converted to a digital signal through the A/D conversion section 32, which is stored into the memory 31.

AMENDMENT UNDER 37 C.F.R. § 1.111
US Application No. 10/665,150
Attorney Docket No. Q77559

Art Unit No. 2884

**Please replace the paragraph beginning on page 23, line 23 and ending on page 24, line 1,
with the following amended paragraph:**

The storage phosphor panel 110 is composed of a storage phosphor layer 111 that will emit an amount of stimulated luminescence in proportion to the radiation energy stored therein when irradiated with excitation light, and a base (substrate) 112 on which the storage phosphor layer is laminated.